



TSMC01-1132

41
2812.
July 31, 2003

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Fr: Stephen B. Ackerman, Reg. No. 37,761
28 Davis Ave.
Poughkeepsie, N.Y. 12603

Subject:

Serial No. 10/002,031 /

11/30/01

Y. C. Yeo et al

"COMPLEMENTARY METAL OXIDE
SEMICONDUCTOR TRANSISTOR
TECHNOLOGY USING SELECTIVE EPITAXY
OF A STRAINED SILICON GERMANIUM LAYER"

GRP. Art. Unit 2812

C.W. Lattin

RESPONSE PATENT OFFICE ACTION

Dear Sir;

In response to the Office Action dated May 30, 2003, please amend the identified
application for patent as follows:

The commissioner is hereby authorized to charge payment of any additional fees involved
with added Claims and the like to Deposit Account No. 19-0033

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal
Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450
Alexandria, VA 22313-1450, on *September 2*, 2003.

George O. Saile Reg. No. *19,572*
Signature *George O. Saile*
Date *9/2/03*

RECEIVED
SEP - 9 2003
TECHNOLOGY CENTER 2800